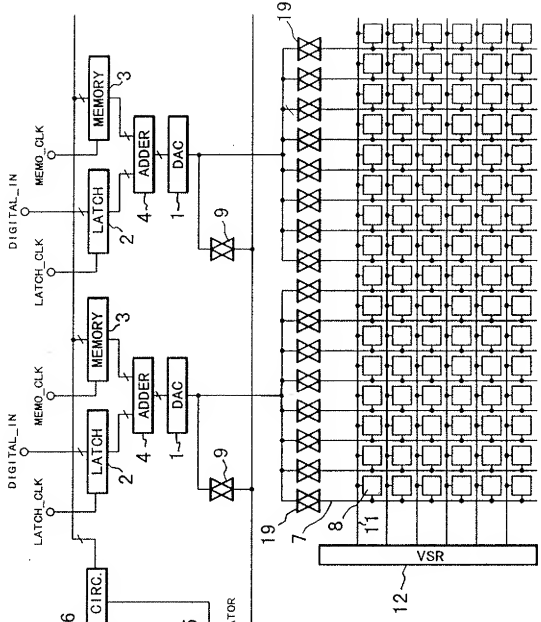
DIGITAL_IN
LATCH_CLK
LATCH
2A
ADDER
4A
DAC
1A
LOGIC CIRC.
6
5
COMPARATOR
MEMO_CLK
MEMORY
3
2
4
1
9
19
7
8
11
VSR
12

FIG. 2